

Product MOSFET

Package SOT-563(EMT6)

| TEST ITEM | $260\pm5^{\circ}$ C , 30sec. , Reflow meth $260\pm5^{\circ}$ C , 10sec. , Solder-bath $350\pm10^{\circ}$ C , 3sec. , Hand solderi $245\pm5^{\circ}$ C ,3sec. , Reflow metho | ng | STANDARD JESD22-A111 | n[pcs] (Sample QTY.) 22 22 22 22 | Pn[pcs] (NG QTY.) 0 0 |
|--|--|--|---|---|--------------------------------|
| | 260±5°C , 10sec. , Solder-bath 350±10°C , 3sec. , Hand solderi 245±5°C ,3sec. , Reflow metho | ng | JESD22-A111 | 22 | 0 |
| | 350±10°C , 3sec. , Hand solderi 245±5°C ,3sec. , Reflow metho | | JESD22-A111 | | |
| Solderability | 245±5℃,3sec.,Reflow metho | | | 22 | 0 |
| Solderability | | d | | | |
| Solderability | | 245±5°C ,3sec. , Reflow method | | 22 | 0 |
| | 245±5°C , 3sec. , Solder−bath | | JESD22-B102 | 22 | 0 |
| Temperature cycle | -55±5°C←→150±5°C 200cycles | | JESD22-A104 | 22 | 0 |
| High temp. high humidity reverse bia | 85±2℃, 85±5%RH, specified bias ,1000hours | | JESD22-A101 | 22 | 0 |
| Pressure cooker test | 121±2°C,100%RH,203kPa,100hours | | JESD22-A102 | 22 | 0 |
| High temperature reverse bias | Tj max., specified bias , 1000hours | | JESD22-A108 | 22 | 0 |
| High temperature gate bias | Tj max, VGSS(max), , 1000hours | | JESD22-A108 | 22 | 0 |
| Intermittent Operation Life | Ta=25±5°C、 ∠Tj≧100°C、 ON 120s/OFF 120s per cycle Duration: 15000cycles | | MIL-STD-750 Method 1037 | | |
| or Power and Temperature Cycle | Ta=-40(+0/-10)°C~+85(+10/-0)°C、 ON 300s/OFF 300s per cycle Duration: 6000cycles | | JESD22 A-105 | 22 | 0 |
| Lead strength (lead pull) | Sample body fixed, pulling lead axis direction , 0.5N , 10 \pm 1sec. | | JEITA ED-4701/400 Test Method 401 | 22 | 0 |
| | | | | | |
| 2. MEASUREMENT ITEM & CRITI | ERIA CONDITION | | CRITERIA | | |
| Gate-Source Leakage : IGSS | Per specification | | 51412101 | | |
| Zero Gate Voltage Drain Current : IDSS | Per specification | According to the electrical characteristics specified by the specification | | | |
| Forward Transfer Admittance : Yfs | Per specification | | | | |
| Physical | Visual check | No outstanding change in physical | sical. | | |
| Solderability | Visual check | Reflow Soldering | Immersed surfase, other than the end of pin as cut-surface, must be covered by solder. | | |
| | | Solder-bath | More than 95% of the electrode must be covered with solder. | | |

No failure is observed from each test item.

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